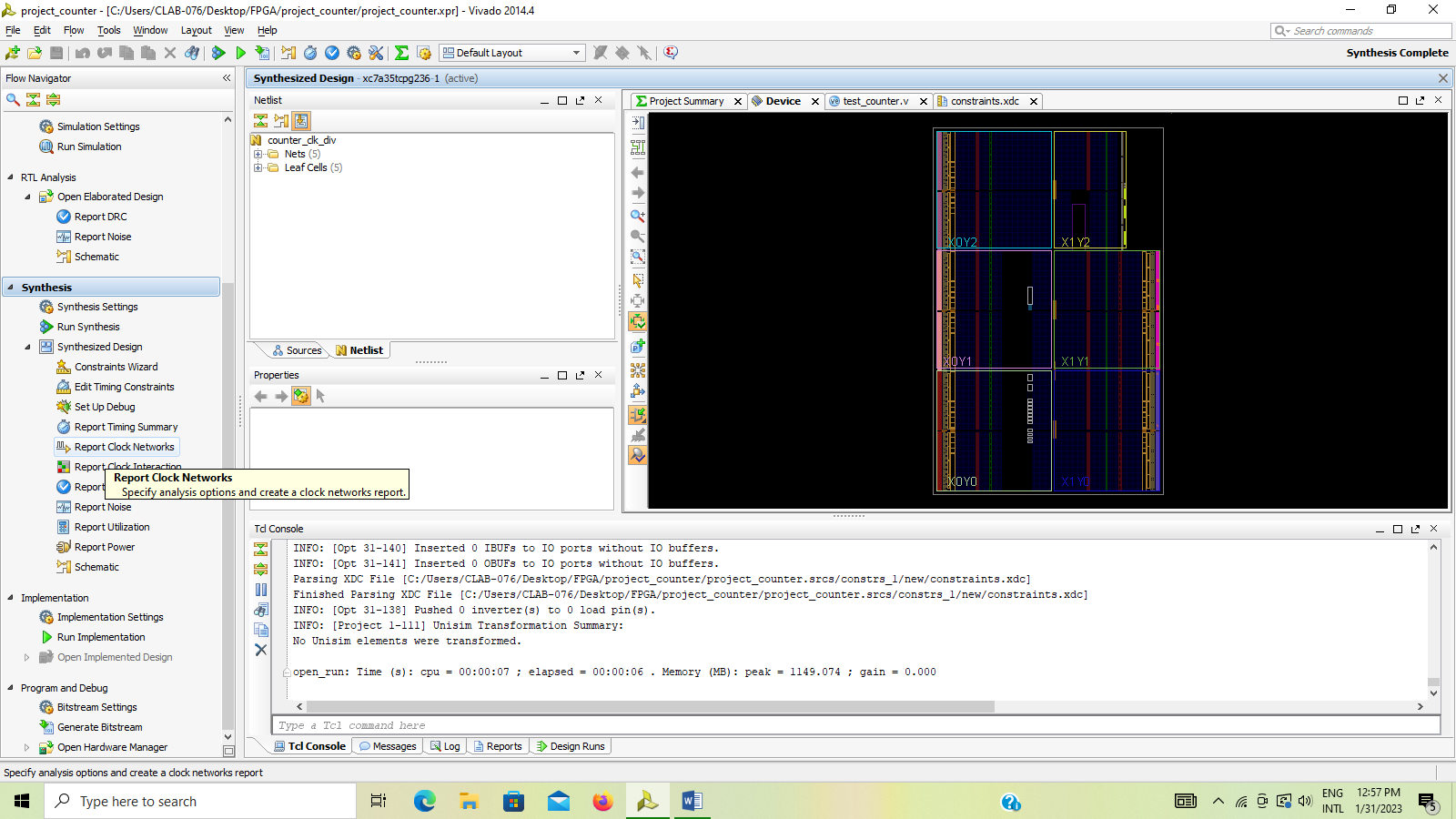
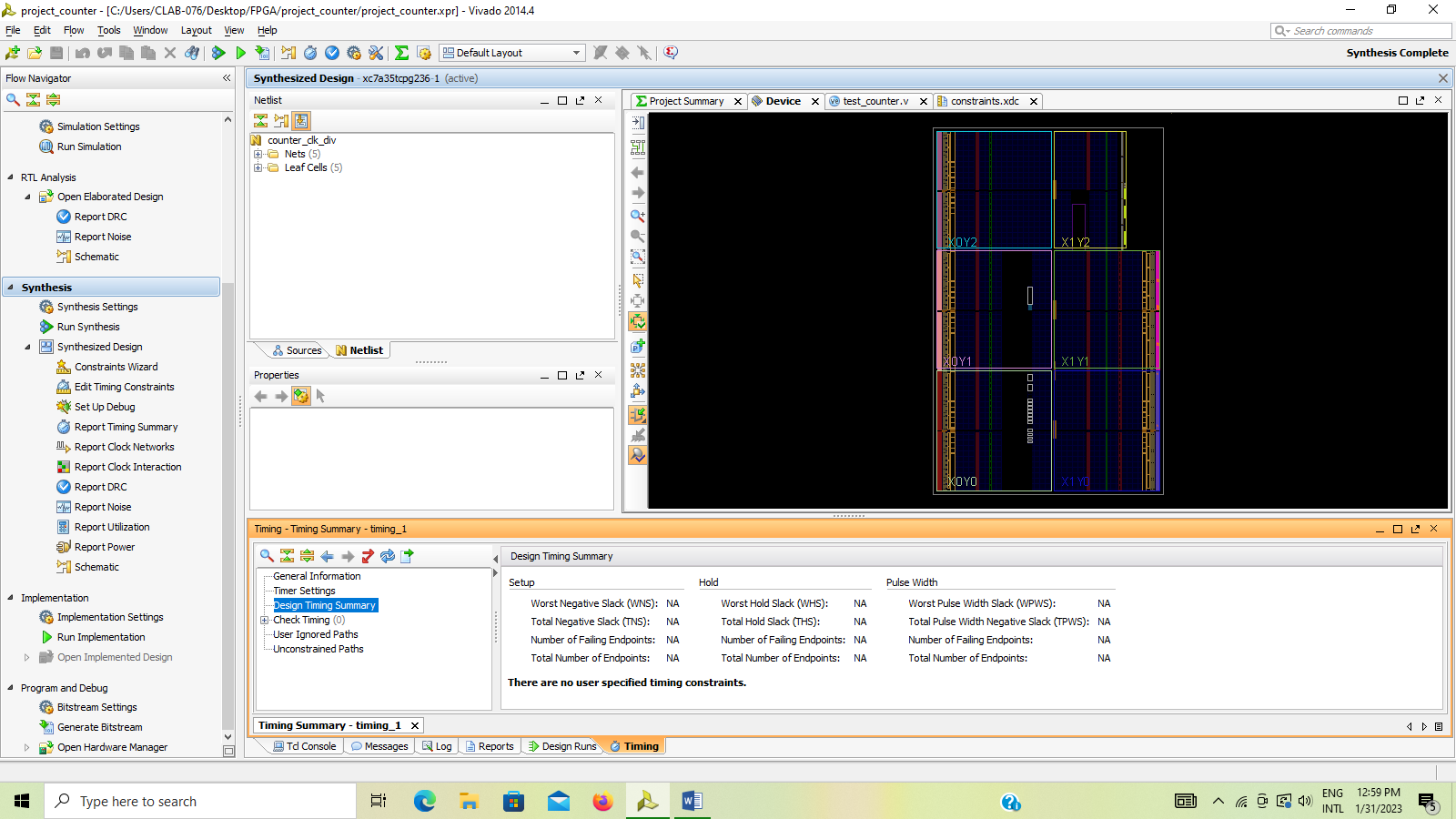


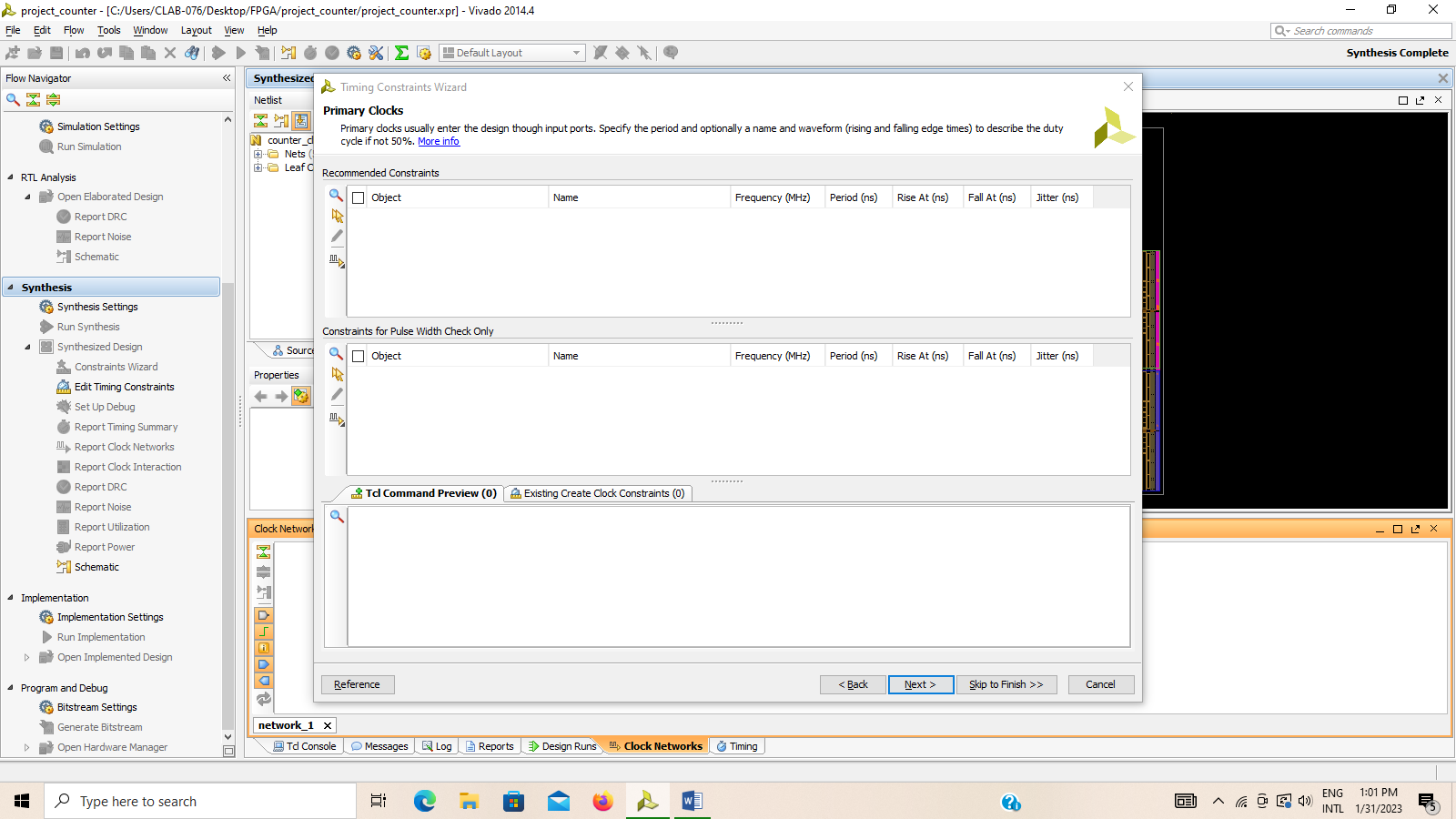
Open the synthesis design



In timing summary

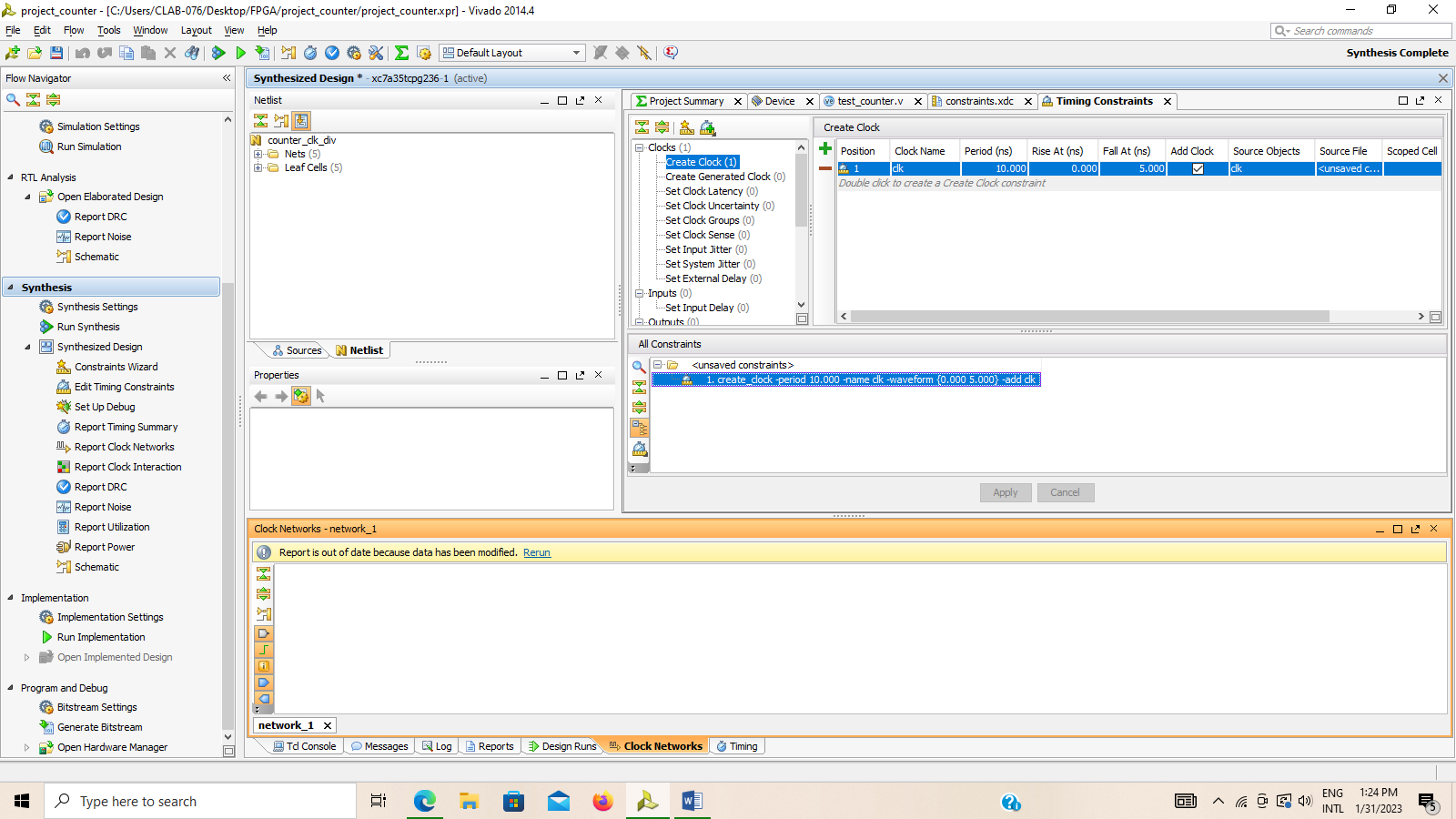
There are no user specified timing constraints.

In Constraint wizard,

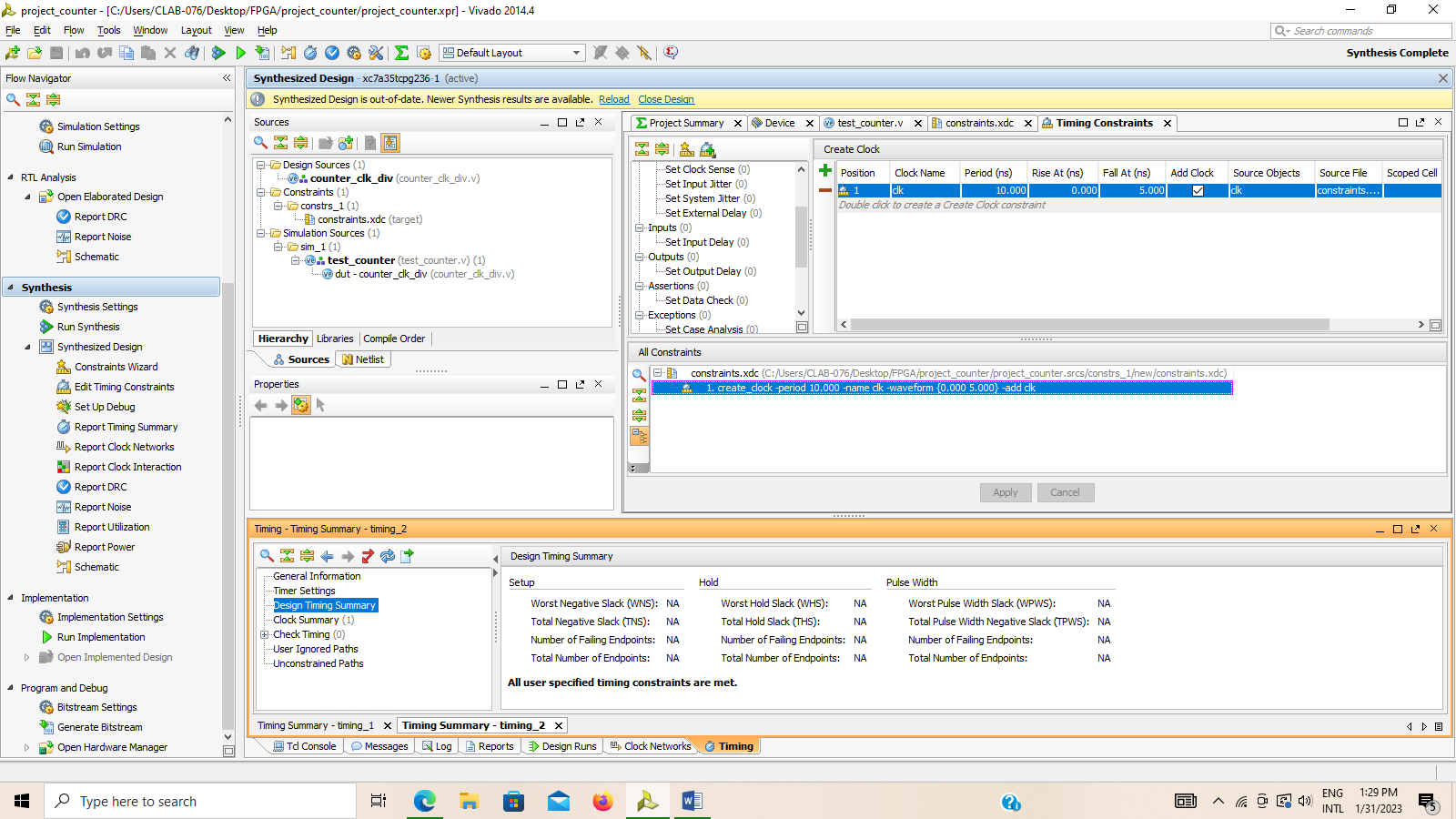


There is no clk information to select.

So Edit Timing Constraints, create clock with 10 ns.



Re run the synthesis



There is still no appear in worst negative slack and other specification.

Schematic is also not correct

